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Applicant  
JOHN HALBERT, et al.

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## U.S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Dr	United States Patent Application Publication No. US 2001/0014925A1, Aug. 16, 2001, Kumata
DG	A 72K CMOS Channelless Gate Array with Embedded 1Mbit Dynamic RAM, Kazuhiro Sawada, et al., pp. 20.3.1 - 20.3.4.

EXAMINER	<i>D. J. Anderson</i>	DATE CONSIDERED	<i>6/25/2003</i>
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